

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No7,115,532 B2
Patent Issue Date October 3, 2006
Application Serial No 10/655,997
Filing Date September 5, 2003
Assignee Micron Technology, Inc.
Inventorship Jon P. Daley
Attorney's Docket No MI22-2380
Title: Methods of Forming Patterned Photoresist layers Over Semiconductor Substrates

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR
APPLICANT MISTAKES and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)**

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 7,115,532 B2, granted October 3, 2006, in accordance with the Certificate of Correction form attached hereto.

It is noted that errors appear in this patent of a typographical nature of character, as more fully described below. The errors occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto, is Form PTO-1050, being suitable for printing.

The exact page and line number where the errors occur in the application file are:

Page 15, paragraph 33;

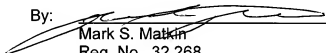
On the Form PTOL-085B filed August 21, 2006 in Section 3, Assignee Name and Residence Data to be Printed on the Patent.

The Director is hereby authorized to charge the \$100.00 fee as required under 37 CFR 1.20(a), or credit any overpayment, to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 7-12-07

By:


Mark S. Matkin
Reg. No. 32,268

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,115,532 B2
DATED : October 3, 2006
INVENTOR(S) : Daley

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page, Assignee:, please delete "Technolgoy" after "Micron" and insert --Technology--.

Title Page 2, OTHER PUBLICATIONS, please delete "10/882,118" after "No." and insert --10/822,118--.

Col. 4, line 26, please delete "the, outer" before "surface" and insert --the outer--.

Col. 6, line 21, please delete "wherein" after "means" and insert --herein--.

Col. 8, line 38, claim 46, please delete "1" after "claim" and insert --29--.

Col. 8, line 42, claim 47, please delete "1" after "claim" and insert --29--.

Col. 8, line 48, claim 49, please delete "1" after "claim" and insert --29--.

Page
1 of 1

Mailing Address of Sender:
Mark S. Matkin
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Patent No. 7,115,532 B2